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	Filing Date		2006-07-26	
	First Named Inventor	Michael Deruginsky		
	Art Unit	N/A		
	Examiner Name	Not Yet Assigned		
	Attorney Docket Number	04305/0204497-US0		

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				Art Unit	N/A
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/DP/	AA*	US-2003/0235315-A1	12-25-2003	Reesor		
	AB*	US-2002/0071578-A1	06-13-2002	Van Der Zwan et al.		
	AC*	US-4,541,112	09-10-1985	Kern et al.		
	AD*	US-5,490,220	02-06-1996	Loeppert		
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	AF*	US-2003/0133588-A1	07-17-2003	Michael Pedersen		

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/DP/	CA	John F. Dickson: On-Chip High Voltage Generation in MNOS Integrated Circuits Using An Improved Voltage Multiplier Technique (5 pages)	

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